

ABSTRACT

A circuit size of a multiplexing apparatus is reduced. In terminal interface units 21a to 23a, data from a terminal is received by each of
5 terminal-side line interface sub-unit 211, and then cellurized by a cell assembly/disassembly sub-unit 28a. The cell is transmitted directly to a priority control unit 242a. In the priority control unit 242a, the cell received from each of
10 the terminal interface units 21a to 23a is stored in each of priority control buffers 243a, and then sent to an ATM network interface unit 24a in the order of priorities sequentially. The cell is synchronized with a line of an ATM network 1a by a
15 network synchronizing sub-unit 241a, and transmitted to the line of the ATM network 1a. With this constitution, an internal bus is unnecessary.